

# Semiconductor nanowire building blocks: From flux line pinning to artificial photosynthesis

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Semiconductor nanowires, by definition, typically have nanoscale cross-sectional dimensions, with lengths spanning from hundreds of nanometers to millimeters. These subwavelength structures represent a new class of semiconductor materials for investigating light generation, propagation, detection, amplification, and modulation. After more than a decade of research, nanowires can now be synthesized and assembled with specific compositions, heterojunctions, and architectures. This has led to a host of nanowire photonic and electronic devices, including photodetectors, chemical and gas sensors, waveguides, LEDs, microcavity lasers, and nonlinear optical converters. Nanowires also represent an important class of nanostructure building blocks for photovoltaics as well as direct solar-to-fuel conversion because of their high surface area, tunable bandgap, and efficient charge transport and collection. This article gives a brief history of nanowire research for the past two decades and highlights several recent examples in our lab using semiconductor nanowires and their heterostructures for the purpose of solar energy harvesting and waste heat recovery.

## Introduction

My personal journey in nanowire research started with Professor Charles Lieber at Harvard University, where I investigated flux line pinning in high  $T_c$  superconductors using nanowires as pinning centers. More than 15 years later, I devote most of my efforts in studying these nanostructures for energy conversion and storage purposes.

I would like to acknowledge the pioneering contribution of R.S. Wagner at Bell Laboratories. Much of our nanowire research today relies on the very powerful method developed by Wagner, known as the “vapor-liquid-solid process” (VLS).<sup>1</sup> In 1964, Wagner described the VLS growth of silicon micro- and nanoscale wires or whiskers and pointed out that one of the catalysts that can be used is gold. Gold is often used today to grow silicon nanowires, as well as copper, nickel, and, more recently, aluminum. Wagner also noted that nanowires can grow into different cross directions. Over the past 10 years, there have

been thousands of papers on silicon nanowire growth, growth direction control, and the use of different catalysts and substrates, all based on Wagner’s original concept.<sup>2</sup> Wagner’s work in the 1960s set the foundation for much of today’s nanowire research.

My first nanowire experiments were in the early 1990s, when many of us were still working on high- $T_c$  superconductors (HTSC). The subject of my PhD thesis was to find a way to introduce linear defects within high-temperature superconductors, in the hope of increasing the critical current density. Our approach was to introduce single-crystalline nanowires into a high- $T_c$  cuprate superconductor to make a composite, to create stable linear tracks, and to increase the critical current density by “pinning” the flux lines.<sup>3</sup> This work paralleled much of the work conducted at the time using fast ion irradiation to create linear defects within high-temperature superconductors. That was the start of my research in this very exciting area, and it

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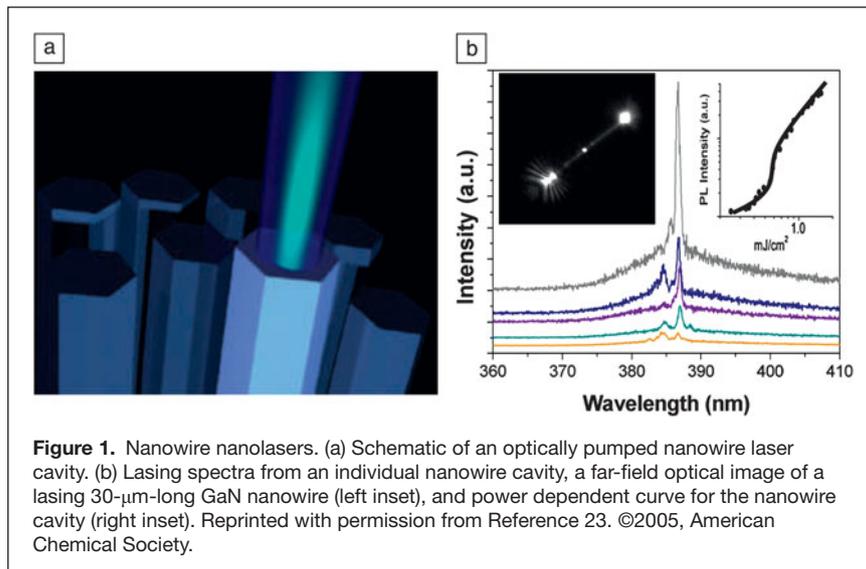
was also the beginning of nanowire research in Lieber's group. Shortly after, the landmark paper by Morales and Lieber (1998)<sup>4</sup> introduced the laser ablation method for the growth of silicon nanowires, once again based on the VLS process. In this case, the vapor was generated using laser ablation. After that, there was significant research using VLS processes to grow semiconductor nanostructures of many different compositions. Many different vapor deposition methods were employed, including chemical vapor deposition (CVD) and several physical vapor deposition techniques.

After joining the University of California, Berkeley faculty in 1999, my first research effort was to investigate the growth mechanism of semiconductor nanowires. Initially, we tried to explore *in situ* growth of Ge nanowires; we observed the process of VLS growth using an *in situ* high temperature transmission electron microscope (TEM);<sup>5</sup> in this case we were investigating the nucleation of a Ge nanowire, using a gold nanoparticle heated to 800°C as the catalyst. Based on the Au-Ge phase diagram, at 800°C, we can first observe an alloying process that leads to the liquid formation; with more Ge vapor incorporation, the system eventually moves into a biphasic region, and we can observe the nucleation event in real time. The creation of this solid-liquid interface is the starting point for one-dimensional crystal growth; this underpins the fundamental nucleation step at the nanometer scale for all of the VLS processes.

In the 1990s, there were very few papers published in the semiconductor nanowire field, but there has been a rapid increase in the past 10 years, and nowadays there are thousands of papers published every year on this subject, with hundreds of research groups now active in this exciting area.

## Nanophotonics

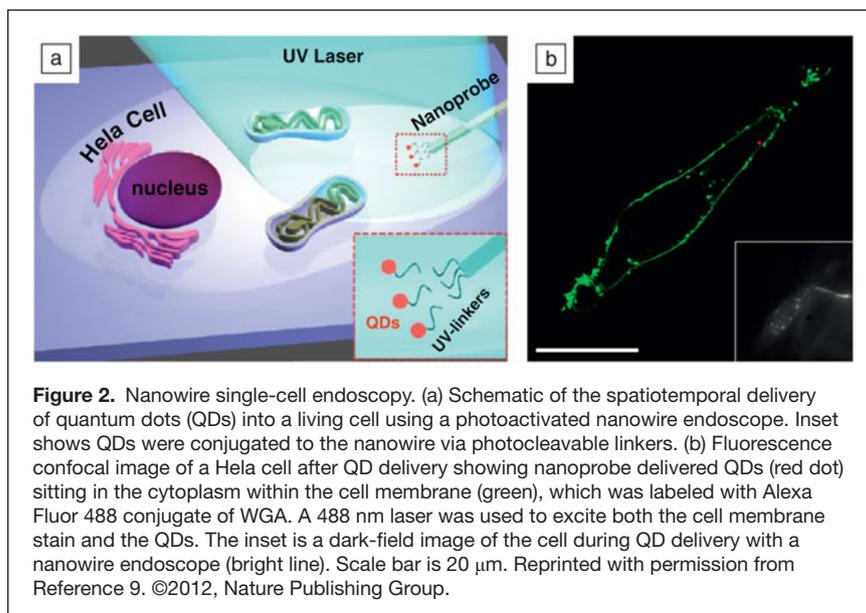
In the last decade, we have seen many important discoveries in a couple of different directions, such as nanoelectronics and photonics, and next I will describe some of the work we did in these areas. In 2000, we discovered that a single-crystalline semiconductor nanowire can serve as a nanoscopic laser cavity (Figure 1).<sup>6</sup> This is basically a Fabry-Pérot cavity, and Figure 1b shows the far-field emission patterns from single nanowires, the power-dependent laser spectra from these individual nanowires, and the integrated intensity as a function of the incident power. For the last couple of years, we have extended our work on photonics from nanolasers to subwavelength waveguides, as well as using a single nanowire to perform nonlinear optical mixing.<sup>7,8</sup> Due to the high refractive index of these nanostructures, we can



**Figure 1.** Nanowire nanolasers. (a) Schematic of an optically pumped nanowire laser cavity, a far-field optical image of a lasing 30- $\mu\text{m}$ -long GaN nanowire (left inset), and power dependent curve for the nanowire cavity (right inset). Reprinted with permission from Reference 23. ©2005, American Chemical Society.

achieve these functionalities either in air or in a liquid.<sup>9</sup> This is quite powerful, as we have basically developed a nanoscopic light source that can be potentially used in a liquid medium.

Recently, we have been trying to utilize these nanoscopic light sources for endoscopy at the single living cell level.<sup>10</sup> We attached a single nanowire onto an optical fiber so that we could deliver light directly to the nanowire waveguide and then use the waveguided end emission to perform imaging at the single-cell level. These nanowires are very robust, do not fracture easily, and can be bent, so they are stable enough to be used in single-cell endoscopy. As illustrated in Figure 2, the nanowire had quantum dots attached to the surface that were cleaved by UV light. After cleaving, the quantum dots were delivered to a single cell. As an example, we can deliver the quantum dots into the cytoplasm and directly to the nucleus. It is



**Figure 2.** Nanowire single-cell endoscopy. (a) Schematic of the spatiotemporal delivery of quantum dots (QDs) into a living cell using a photoactivated nanowire endoscope. Inset shows QDs were conjugated to the nanowire via photocleavable linkers. (b) Fluorescence confocal image of a HeLa cell after QD delivery showing nanowire delivered QDs (red dot) sitting in the cytoplasm within the cell membrane (green), which was labeled with Alexa Fluor 488 conjugate of WGA. A 488 nm laser was used to excite both the cell membrane stain and the QDs. The inset is a dark-field image of the cell during QD delivery with a nanowire endoscope (bright line). Scale bar is 20  $\mu\text{m}$ . Reprinted with permission from Reference 9. ©2012, Nature Publishing Group.

then possible to perform high spatial-resolution imaging. Using the nanowire as a scanning probe in a liquid environment is a powerful imaging tool. I believe that in the next few years, we will continue to see many new developments that integrate all of these active nanoscopic optical components into integrated nanophotonic circuits and also use the nanowire probe at the single-cell level for targeted delivery, imaging, or even stimulating single living cells.

### Thermoelectric energy conversion

I want to focus now on how these nanoscale one-dimensional structures can be used for terawatt energy conversion and storage. We all know that the amount of energy required worldwide is on the scale of terawatts, and the percentage of renewable energy in the current energy portfolio is very limited. Many research groups are now working on ways to save energy or convert renewable energy. My group approaches this problem from two different directions. The first relates to saving energy by developing nanostructured silicon thermoelectrics for waste heat recovery, and the second, more recent effort, involves developing nanostructures for solar energy conversion, either directly into electricity or into liquid fuels through artificial photosynthesis.

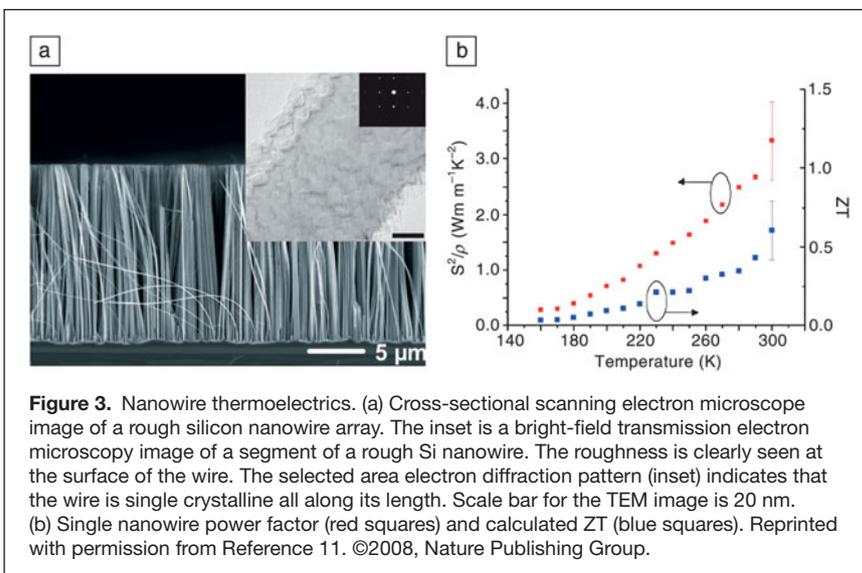
If we examine the performance of power plants delivering 15 TW of power through fossil fuels, we find the total energy expended is 38.6 TW with 40% efficiency for the heat engine. This means that about 20 TW is lost as waste heat to the environment. The question is whether we can develop a technology to recover some of this waste heat energy? A thermoelectric material provides a suitable strategy for this. If we had a thermoelectric waste heat recovery module with an efficiency of 3%, we could recover up to 1 TW of energy; this is a potential solution to the terawatt energy challenge, provided we can find the right thermoelectric material to achieve this.

We started to work in this field about 10 years ago, together with Arun Majumdar in the Mechanical Engineering Department at Berkeley. We first examined the thermal transport in a single-crystalline silicon nanowire. For any thermoelectric material, the important figure of merit is the dimensionless parameter  $ZT$ , which is related to the Seebeck coefficient, the electrical conductivity, and the thermal conductivity of the material. A high  $ZT$  value means high energy conversion efficiency. We considered using one-dimensional structures to control thermal transport, essentially to suppress the thermal conduction, while allowing all the carriers, electrons, or holes to transport and maintain a reasonable level of the Seebeck coefficient. The design principle, for Si in particular, was to choose a wire diameter that is smaller than the phonon mean-free path (200–300 nm) but larger than the electron mean-free path (1–2 nm). We measured the thermal conductivity of individual nanowires using a

suspended microelectromechanical system (MEMS) device by heating one end and then measuring the thermal transport to the other end of the MEMS platform. By measuring the steady-state temperature on the other end, we can calculate the thermal conductance of the nanowire thermal bridge, normalize it to the nanowire cross-section, calculate the thermal conductivity, and then examine the size dependence of these semiconductor structures.

The initial results were very promising.<sup>11</sup> This was the first study of size-dependent thermal conductivity of Si nanowires. The thermal conductivity of the Si nanowires was suppressed by an order of magnitude compared to bulk Si. By simply decreasing the diameter of the nanowire, we could decrease the thermal conductivity through phonon-boundary scattering. However, the thermal conductivity was still quite high, typically at the level of  $10 \text{ Wm}^{-1}\text{K}^{-1}$ , which is not sufficient for a good thermoelectric material. Later, after several years of exploring various strategies to further decrease the thermal conductivity, we started investigating a solution-based process for the production of silicon nanowires with a rough surface, which was found to be critical for further thermal conductivity reduction.<sup>12</sup> This is a simple etching process that creates a rough surface on the silicon nanostructure. It led to the large scale production of rough silicon nanowires; they can now be made on six-inch wafers. The nanoscopic surface roughness turns out to be the key structural feature to further decrease the thermal conductivity, without significantly decreasing the Seebeck coefficient and electrical conductivity of these nanowires. These nanowires now have values of  $ZT$  that are two orders of magnitude greater than that of bulk Si and reach a value of 0.6, as shown in **Figure 3**.

In 2009, together with a Berkeley graduate student, I founded the Alphabet Energy Company, and we are already producing modules using these Si nanostructures. The company currently employs 16 people and is continuing to hire, so nanowire technology is indeed creating jobs. We will have our



first pilot thermoelectric testing in mid-2012. The prospect of using these nanostructured silicon thermoelectrics for waste heat recovery is real.

### Conversion and storage of renewable energy

I will now concentrate on the semiconductor nanostructures required to convert and store renewable solar energy. Since we are aiming to operate at the terawatt level, the amount of solar energy required is enormous, and this must be achieved with reasonable efficiency and at a reasonable cost. Before discussing the use of nanowires for this purpose, I want to emphasize some of the fundamental materials requirements for designing solar energy conversion devices. First is the use of the earth's abundant elements, which will be absolutely necessary if we are to work on the terawatt scale; and second, whether we are developing solar-cell devices or an artificial photosynthetic system, we need to use less-energy-intensive processes to make these materials and to create the necessary systems. Where possible, we want to use mild chemistry or mild processing conditions and minimize the energy used to create these energy conversion devices; this applies to both photovoltaic (PV) applications and artificial photosynthesis.

We started our research on nanowire-based solar cells about seven years ago. The first example was to use a dye-sensitized cell (DSC) as a test platform, with single-crystalline nanowires as the electrodes, replacing the traditional TiO<sub>2</sub> nanoparticle percolation network.<sup>13</sup> One of the major advantages of using the single-crystalline nanowire conduction channel in a DSC cell is that electron diffusivity is enhanced, compared to the nanoparticle percolation networks. In recent years, there have been various research efforts utilizing nanowires in composite solar cells or in a core-shell geometry. Nanowire structures have many benefits; it is fairly straightforward to design a suitable interface to facilitate charge separation, and the charge collection can also be quite efficient. In addition, the core-shell nanowire geometry has the advantage of orthogonalized light absorption and charge separation, a feature quite important for material systems with short minority carrier diffusion length. For nanowire arrays with a suitable periodicity, we can introduce a light-trapping mechanism so that less material will be needed for light collection.

With all the benefits of using nanowires as the PV active elements, we should not forget that we are also introducing an important potential limitation—the large junction area. These large interfacial areas in nanostructured solar cells naturally introduce significant trapped states and various channels for carrier recombination. If we examine the performance of these nanostructured PVs, we typically find that the fill factor is low, and the open circuit voltage ( $V_{oc}$ ) is low. This is intrinsically related to the high junction surface area solar cell and is particularly obvious in the nanowire polymer composite solar cell, where low fill factors and  $V_{oc}$  were typically reported. To mitigate this problem, we developed a simple solution procedure to produce high quality nanowire core-shell solar cells with excellent fill factor and  $V_{oc}$ .<sup>14</sup> We first make CdS nanowires in

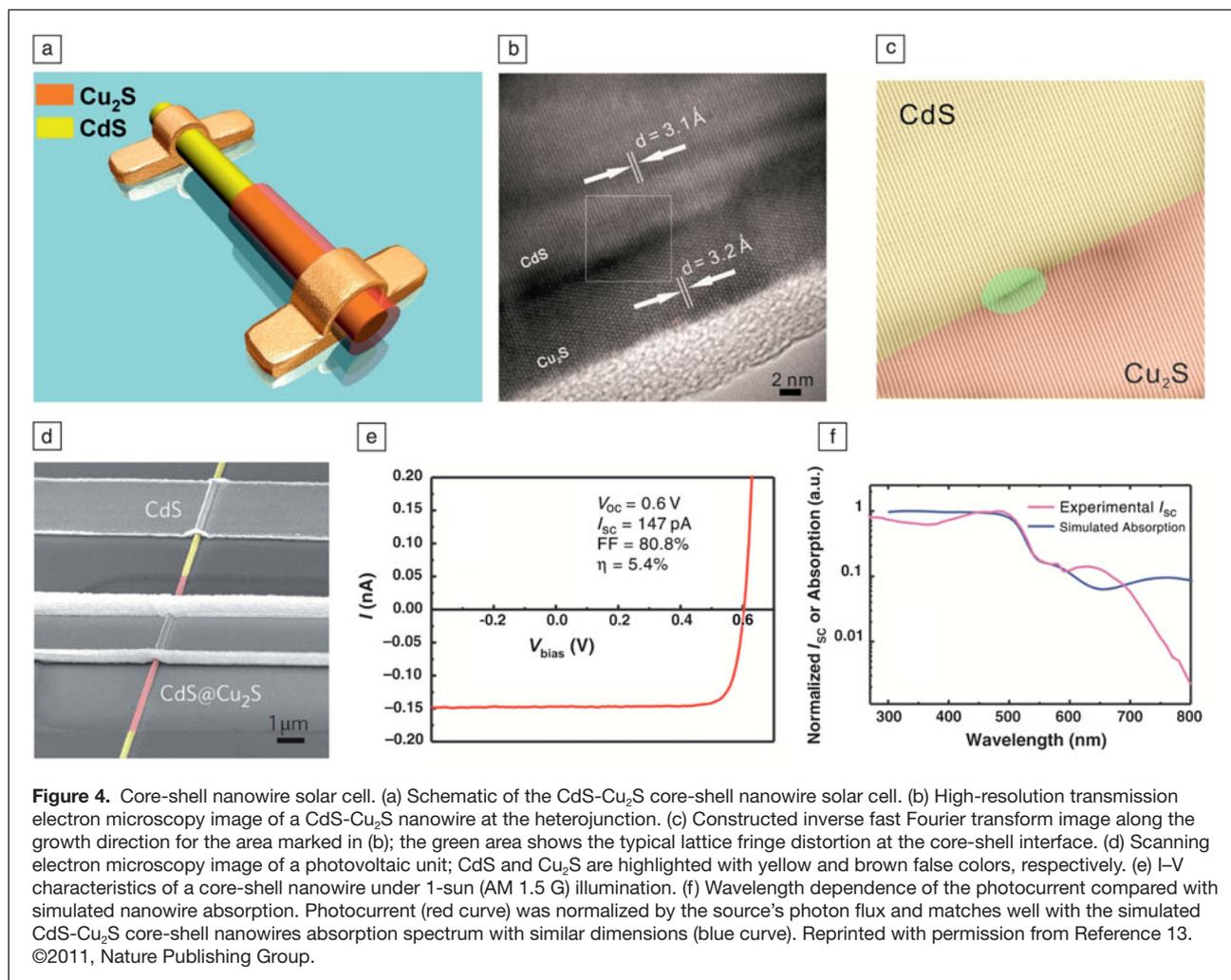
solution, then we use a mild cation-exchange chemistry to produce the core-shell nanostructure, as shown in **Figure 4**. This cation exchange chemistry was developed in Paul Alivisatos's group at the University of California, Berkeley. All that is required is to dip a nanowire for a couple of seconds in a copper solution at 50°C; the core-shell nanowires can then be fabricated into an asymmetrical junction to measure PV performance.

It is interesting to note that we can create the interface using mild processing conditions. The resulting interface is highly coherent, with a minimum amount of defects or dislocations. The CdS-Cu<sub>2</sub>S interface is illustrated in Figure 4b–c. The heterojunction prepared by this method is atomically well-defined with few interface defects, which enables excellent charge separation and minimal minority carrier recombination. As a result, our nanowire PV device shows excellent  $V_{oc}$  and fill factors. Figure 4e shows the I–V curve from the individual nanowires, and the current output is limited by absorption in the visible region, which is limited in turn by the thickness of the Cu<sub>2</sub>S shell (Figure 4f). However, the most important message here is that the fill factor and  $V_{oc}$  of this device are even better than those for traditional thin films. The insight we gained here is that we can indeed create highly coherent interfaces using mild chemistry and less-energy-intensive processes. Such a coherent interface is the key structural feature that could lead to high fill factor and  $V_{oc}$  for nanostructured solar cells. I believe that with this strategy, we will be able to push the efficiency above 10% by adding more visible light absorbing Cu<sub>2</sub>S.

### Artificial photosynthesis

For PVs, we will have to worry about the energy storage issue because of the intermittency of sunlight irradiation. It would be much more appealing if somehow we can convert the solar energy directly into liquid fuels. This is artificial photosynthesis, where we try to mimic what is happening in nature: to convert solar energy and store it in chemical form, possibly in the form of hydrogen, methanol, or even gasoline. In our daily life, we use gasoline in cars, and as a result, CO<sub>2</sub> is emitted into the environment. Artificial photosynthesis attempts to convert the CO<sub>2</sub> and water back into liquid fuel, which will be a truly carbon neutral process. Once again, in this artificial photosynthesis process, we have to emphasize the need to use earth abundant elements and more energy efficient processes.

If we examine the chemical reactions required to generate solar fuels, either by water splitting or CO<sub>2</sub> reduction (where there are many possible products), they are all “uphill” reactions, not spontaneous; we will need solar energy as the energy input and catalysts to drive these reactions. Therefore, in order to generate solar fuel by artificial photosynthesis, we need two things: the first is quite similar to PVs; we need to have the semiconductor and the junctions for light harvesting and charge separation. Then we need to utilize the charge carriers to run the reactions, instead of extracting them as electricity as in solar

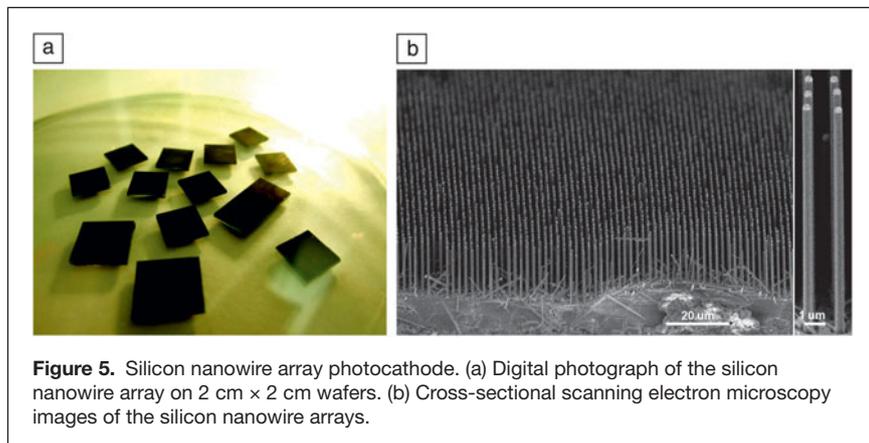


cells. If we consider the materials challenges in developing this artificial photosynthesis system, we find many of them are relevant to the overall work performed by the materials research community at large. Two examples are photocathode materials for hydrogen and CO<sub>2</sub> reduction and photoanode materials for water oxidation. The photoanode materials are particularly challenging, and we do not have a solution at this moment. We also need to develop better catalysts for water reduction, CO<sub>2</sub> reduction, and water oxidation. In order to balance the ion flux during this artificial photosynthetic process, we also need an ion conductive membrane that is gas impermeable, mechanically stable, and optically transparent, and this must sit between the light absorbing photoanode and photocathode.

Semiconductor nanowires, with their high charge mobility and high surface area, are good candidates to serve as the scaffold for the entire artificial photosynthetic system. Solar water-splitting research commenced with Fujishima and Honda using TiO<sub>2</sub>,<sup>15</sup> which is a UV absorbing material. Although the basic concept works quite well, this will never result in a high efficiency photoelectrochemical (PEC) cell because the fraction of UV photons in the solar spectrum is limited. Nozik<sup>16</sup> proposed the PEC diode concept based on the concept of a coupled

photocathode and anode, with an ohmic contact in between so that minority carriers can be used on both sides to run the water reduction and the water oxidation for complete solar water splitting. This two-photon process essentially mimics the photosynthetic process in nature; now we can place two small bandgap materials back-to-back in a diode configuration and make use of the UV and visible spectrum. The major advantage here is the increased photocurrent from the coupled photocathode and anode system, compared to the single bandgap structure. The single bandgap structure is limited to the UV region, while in the two-bandgap system we can make use of the entire solar spectrum, and current density at a 20 mA/cm<sup>2</sup> level is possible. Our main goal was to capture the entire solar spectrum and use it for solar fuel conversion.

We have a couple of good candidates for the photocathodes. The Si nanowire array is certainly very promising; **Figure 5** shows black-colored silicon nanowire arrays in the form of 2 cm × 2 cm wafers. We can make these ordered nanowire arrays shown in Figure 5b, and decorate them with water reduction catalysts such as Pt or MoS<sub>2</sub> nanoclusters. These devices have reasonable photocurrent output up to 20 mA/cm<sup>2</sup>, and we can implement the “half reaction” of hydrogen generation



by using a tunable bandgap material to cover the solar spectrum, we will be able to develop a useful photoanode. The initial testing on the photocurrent output is quite disappointing, in the range of  $50 \mu\text{A}/\text{cm}^2$ , far below that from what we would expect based on their visible bandgap.<sup>21</sup> We have short carrier lifetime issues and extraction issues here, and clearly these  $\text{In}_x\text{Ga}_{1-x}\text{N}$  nanostructures need to be optimized for photoanode applications.

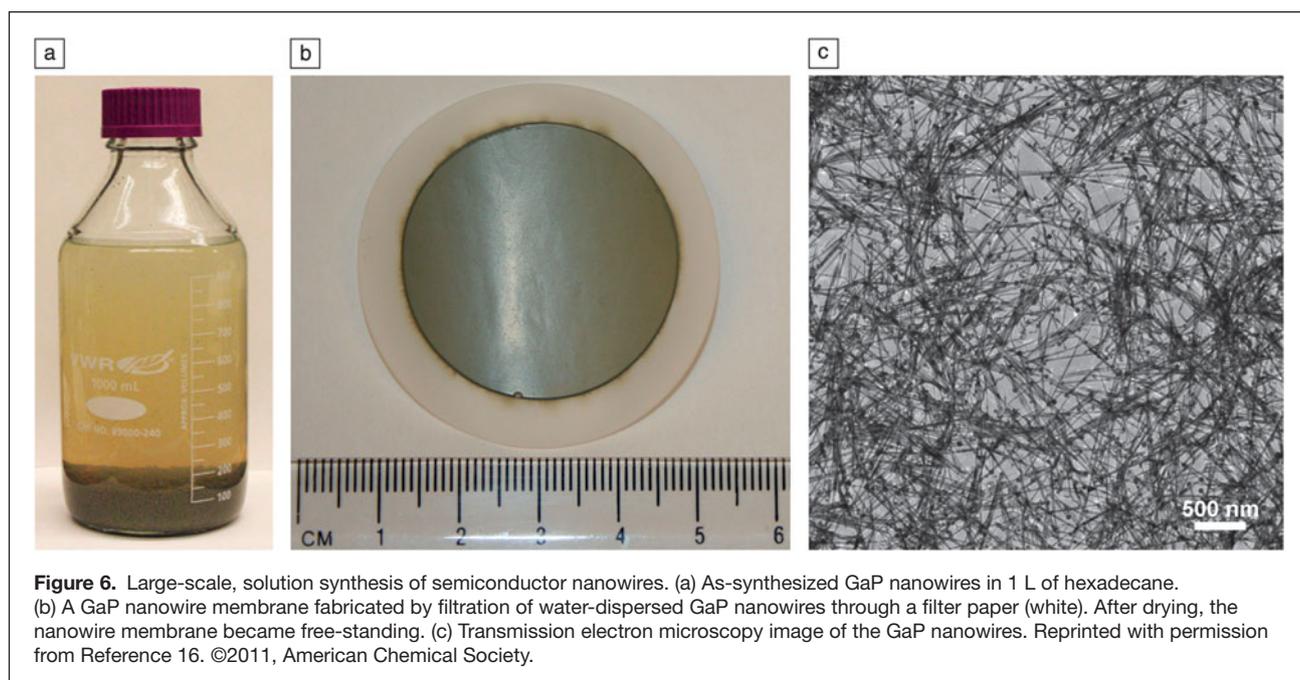
Finally, I will briefly discuss the way we plan to implement this two-bandgap configuration. We have two schemes: the first is an asymmetric Si-TiO<sub>2</sub> core-shell model system; the second is based on bilayer nanowire fabrics.

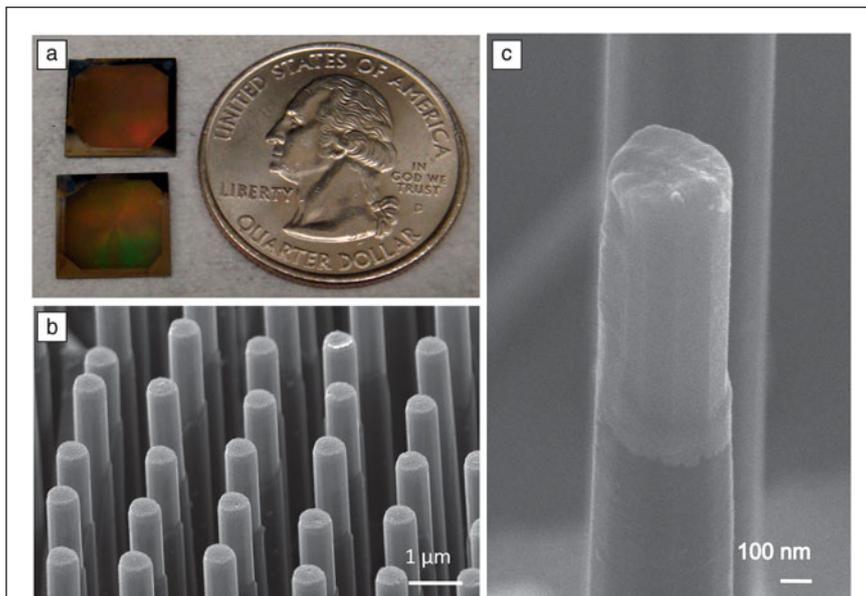
this way.<sup>22</sup> More recently, we have also investigated possible photocathodes for CO<sub>2</sub> reduction. For the CO<sub>2</sub> reduction, we chose GaP because CO<sub>2</sub> redox potential is more negative than the hydrogen redox potential. For this purpose, we developed a surfactant-free solution process to generate a large quantity of single-crystalline GaP nanowires (Figure 6).<sup>17</sup> We can now produce these GaP nanowires without an organic coating, and we can control the doping by adding a zinc precursor as well. We believe that a combination of Mo<sub>3</sub>S<sub>4</sub> cubane molecular catalysts and high surface area GaP nanowires can lead to a very promising photocathode.

The anode, however, presents more of a problem, because we have a very limited choice of material. The best candidate is Si-doped Fe<sub>2</sub>O<sub>3</sub> from Michael Grätzel's lab at the Ecole Polytechnique de Lausanne in Switzerland, for which photocurrent is typically in the single digit mA/cm<sup>2</sup> range.<sup>18</sup> Our group has been working on  $\text{In}_x\text{Ga}_{1-x}\text{N}$  nanostructures with a tunable bandgap from 3.3 eV to 1 eV.<sup>19</sup> We are hopeful that,

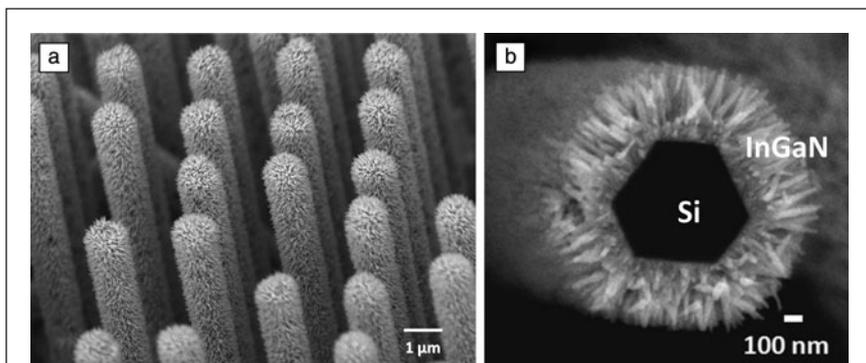
In the first case, the Si/TiO<sub>2</sub> asymmetrical junction represents a fully integrated cathode and anode in a single nanowire (Figure 7);<sup>20</sup> Si can be decorated once again by the reduction catalyst, and these arrays can be used directly for water splitting. This process is based on CVD; large reactors can be used to make larger arrays. It is, however, quite energy intensive to create these asymmetrical junctions. We are also investigating silicon nanowires decorated by high surface area InGaN nanorods (Figure 8),<sup>21</sup> and this will be our model system for balancing the photocurrent from the anode and cathode. However, contact between these two materials remains to be optimized.

We recently embarked on the concept of bilayer nanowire fabrics for PEC applications; everything here is processed in solution, and the fundamental idea is quite similar to the asymmetric junction. Basically, we want to directly couple the photoanode and photocathode, both made of nanowire mesh; one side will perform the water oxidation, the other side will be used for water reduction, and a proton transport medium will





**Figure 7.** Si/TiO<sub>2</sub> asymmetric core-shell nanowire array. (a) Optical image of asymmetric nanowire array made using soft lithography, showing iridescent color due to array periodicity. Scanning electron microscopy images of an asymmetric nanowire array (b) and an individual nanowire (c). Reprinted with permission from Reference 19. ©2011, American Chemical Society.



**Figure 8.** High surface area Si/InGaN nanowire photoanodes. Scanning electron microscopy images of hierarchical Si/In<sub>x</sub>Ga<sub>1-x</sub>N nanowire arrays on a Si (111) substrate with X = 0.08–0.1 (a). A fractured wire reveals the cross-section (b) showing that InGaN nanowires grow vertically from the six Si wire facets. Reprinted with permission from Reference 20. ©2012, American Chemical Society.

be placed in between. We can now make two-inch diameter discs by laminating two layers of nanowire materials together, for example, a WO<sub>3</sub> or TiO<sub>2</sub> nanowire mesh on top and a GaP nanowire mesh underneath, where one is the photocathode and one is the photoanode. Of course the bandgap and photocurrent output do not match at this point. We are trying to use this as a model system to demonstrate whether such a bilayer nanowire fabric idea can work. The initial results are promising. The main problem right now is the urgent need to discover better anode materials to replace TiO<sub>2</sub> or WO<sub>3</sub>. Above all, we believe that these high surface area semiconductor nanowires, with their high carrier mobilities, will eventually be part of the artificial photosynthesis system as these nanostructures enable the

stacking of the catalysts in the third dimension, and effectively relax the stringent requirements for catalysts with high turnover frequency.

## Summary

Over the past 10 years, we have continued to develop the science and technology of semiconductor nanowires. As of today, nanowires with different sizes, growth directions, compositions, and heterojunctions can be rationally designed, synthesized, and assembled. We have already seen major progress in many different areas of nanowire research (e.g., electronics, photonics); we can be confident that we will continue to see many more fundamental new discoveries and science based on this unique class of nanoscale building blocks. Considering their unique structural, optical, and electrical properties, we expect that these nanostructures will have a significant impact on large scale clean energy conversion and storage technologies. In the meantime, the future of nanowire technology will be largely dependent on how well we can balance the issues of cost, performance, and stability of nanowire-based devices and systems.

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